

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated May 8, 2009, has been received and its contents carefully reviewed.

Claims 1-11 are canceled, and claims 18-35 are withdrawn from consideration in this application. Claims 12-17 are rejected by the Examiner. Claims 12-17 have been amended. Claims 12-35 remain pending in this application.

In the Office Action, claims 12-17 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 4,279,035 to Skerlos (hereinafter "Skerlos") in view of U.S. Patent No. 5,713,040 to Lee (hereinafter "Lee") and a computer-generated English translation of Japanese Patent Pub. No. 07-086893 A to Kida (hereinafter "Kida").

The rejection of claims 12-17 is respectfully traversed and reconsideration is requested. Claims 12, 14 and 16 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "...generating a detection reference signal to compare periods of the input signal and a pre-synchronizing signal." None of Skerlos, Lee and Kida teach or suggest at least this feature of the claimed invention.

Also, the Examiner alleged that Kida discloses a method for verifying a pulse signal, comprising the steps of: generating a detection reference signal (e.g., C); and analyzing an output pulse signal (e.g., B) during an input interval of the detection reference signal (e.g., C) to verify the outputted pulse signal (e.g., B).

But, in the Kida, the comparator 3 compares the cycle time of the fixed pattern signal B with the cycle time of the simulated clock signal C. The simulated clock signal C of Kida is a comparative object to compare with the fixed pattern signal B.

In the present invention, the signal present comparator 58 compares the number of pulses of the signal of the first state with the predetermined plural number during the input interval of the detection reference signal. The detection reference signal is used to determine the period of comparing the number of pulses of the signal of the first state with the predetermined plural number, not a comparative object.

None of Skerlos, Lee and Kida teach or suggest at least this feature of the claimed invention. Accordingly, claims 12-17 are allowable over Skerlos, Lee and Kida.

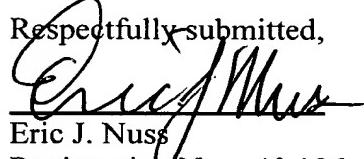
Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: August 7, 2009

Respectfully submitted,



Eric J. Nuss
Registration No.: 40,106
McKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorneys for Applicant